

FIG. 1

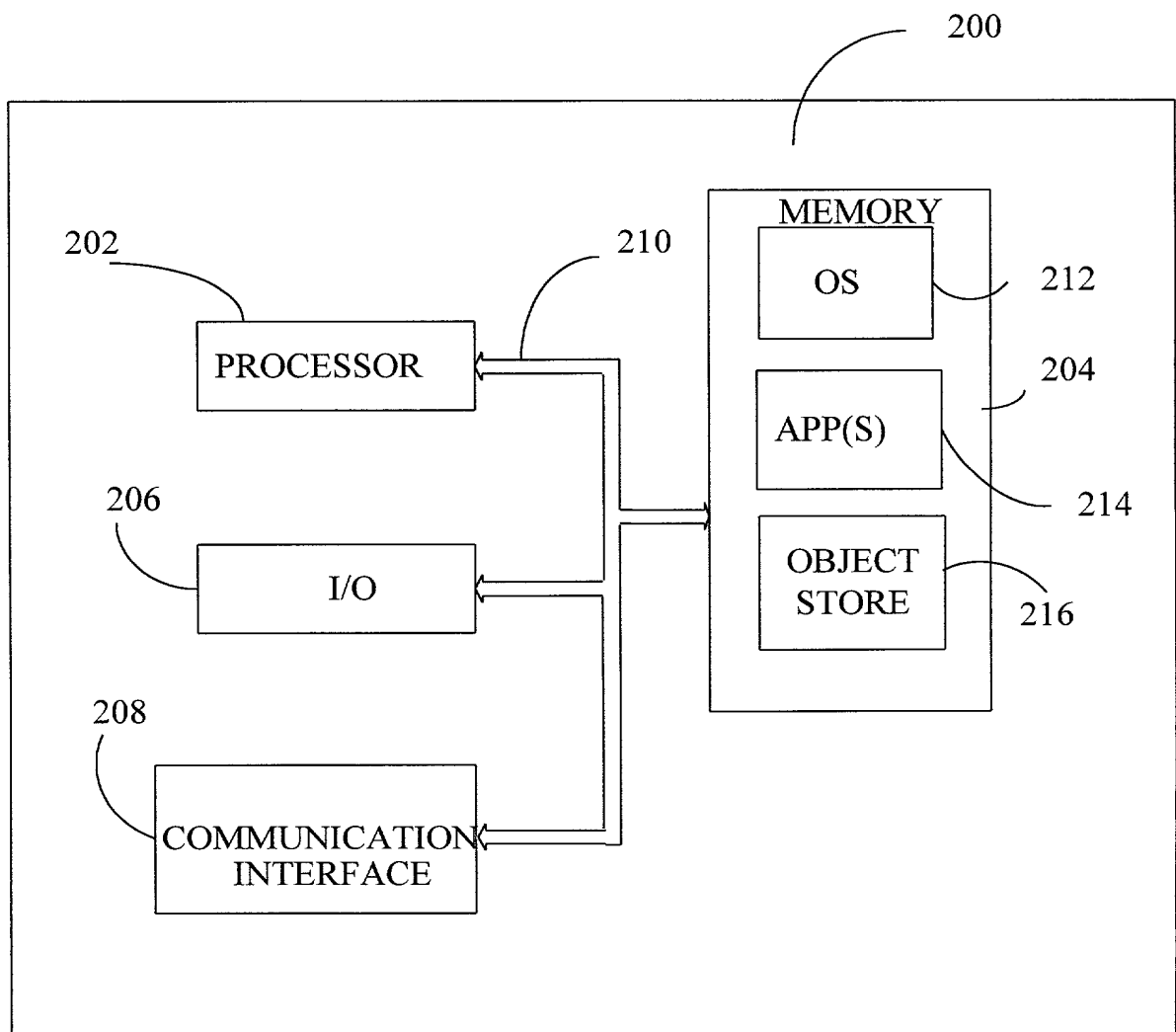


FIG. 2

FIG. 3 is a block diagram of a system for processing a speech signal. The system includes an application (300) that interacts with a context-free grammar engine (302) and a speech recognition engine (304). The application (300) is connected to a compiler back-end (312) and a compiler front-end (311). The compiler back-end (312) generates binary formatted grammars (308) which are used by the context-free grammar engine (302). The compiler front-end (311) is connected to a text formatted grammar (314). The speech recognition engine (304) receives a speech signal (306) and outputs a speech signal (306) to the application (300).

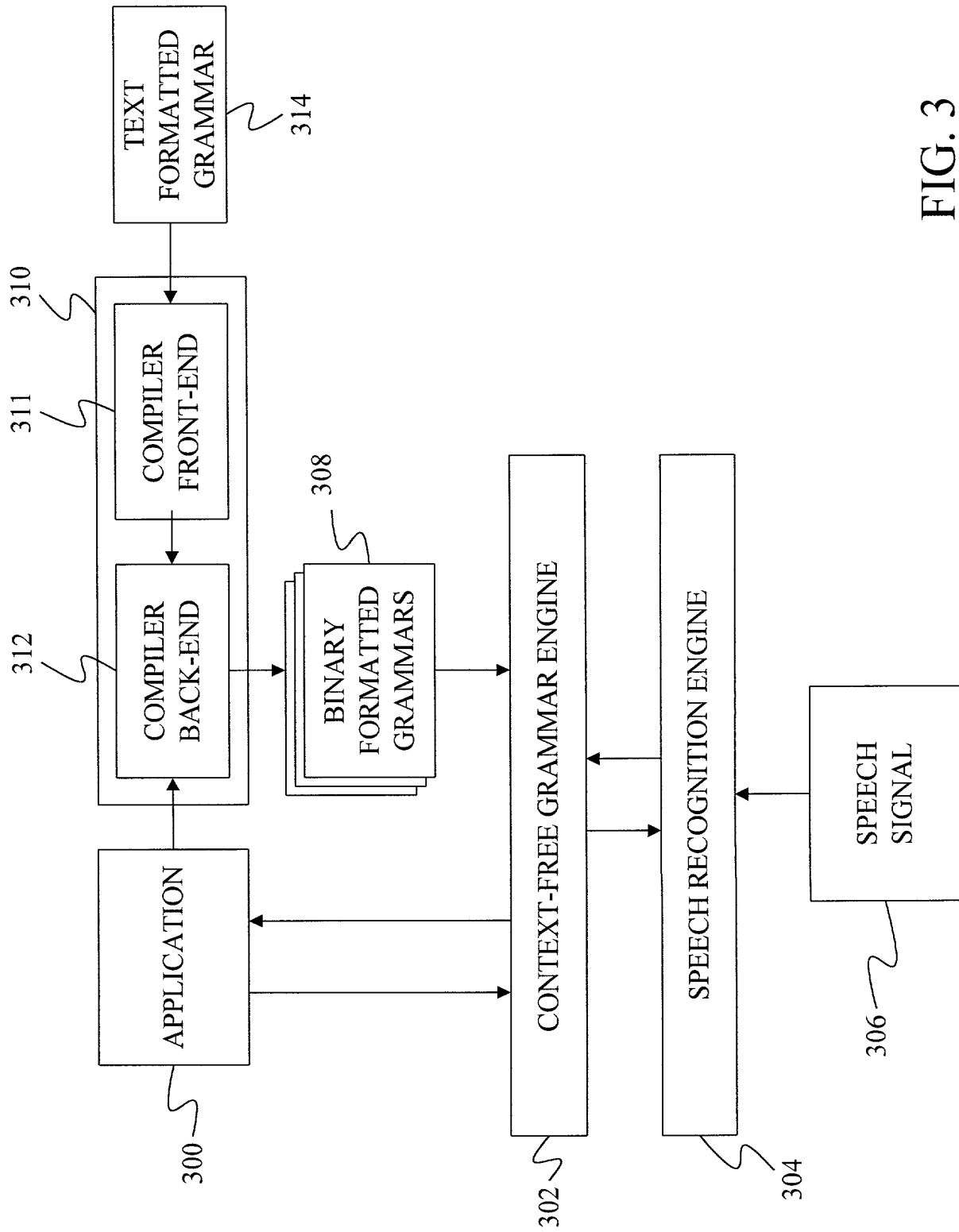


FIG. 3

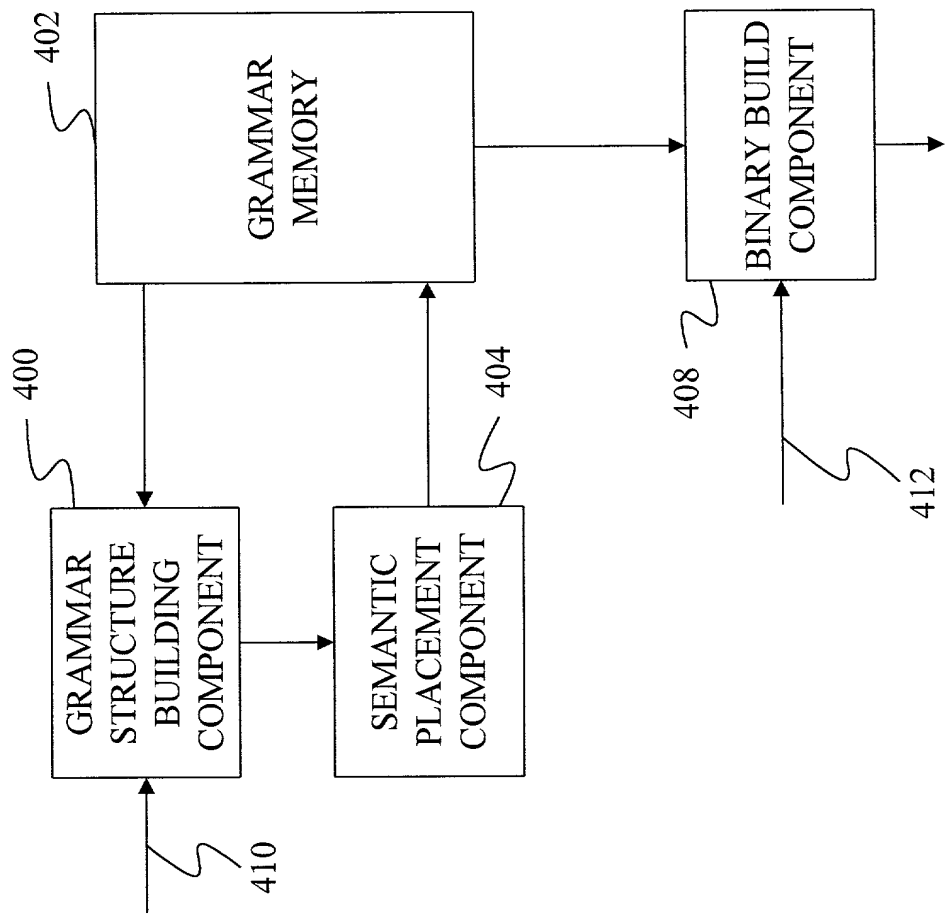


FIG. 4

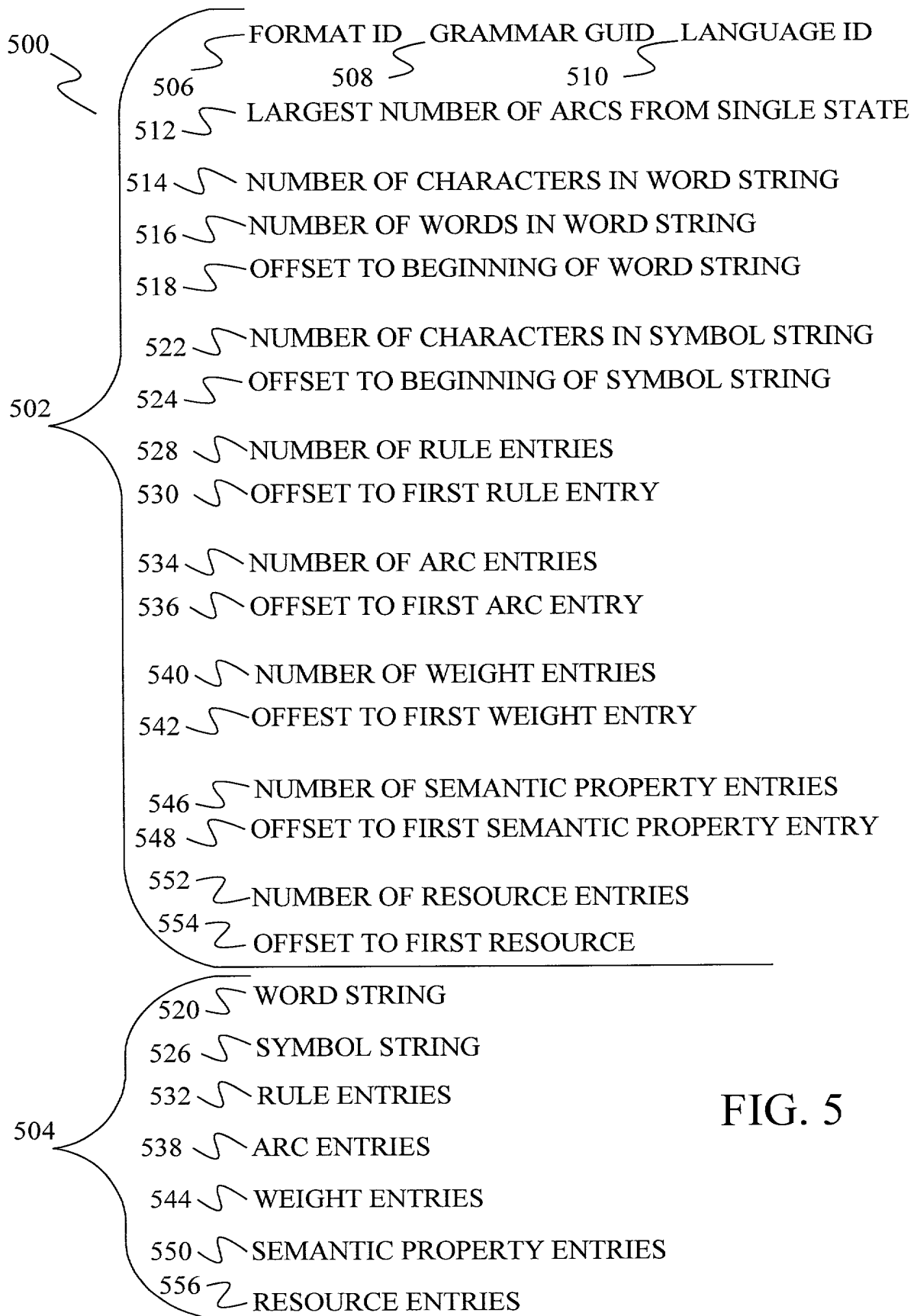


FIG. 5

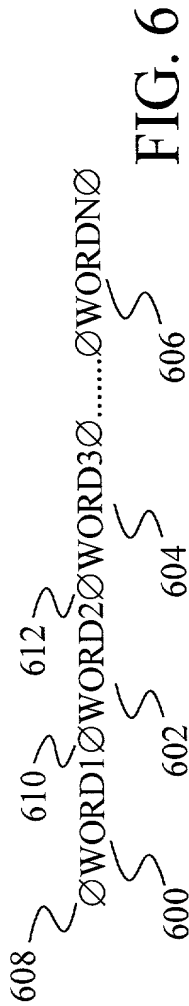


FIG. 6

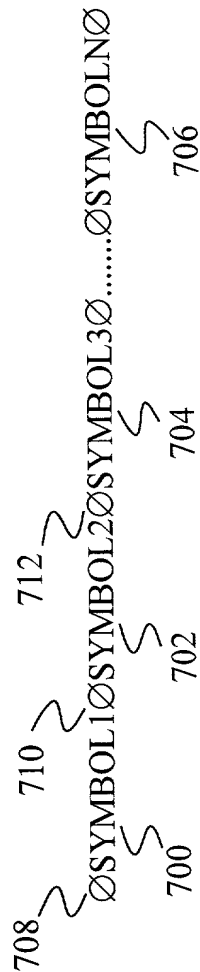


FIG. 7

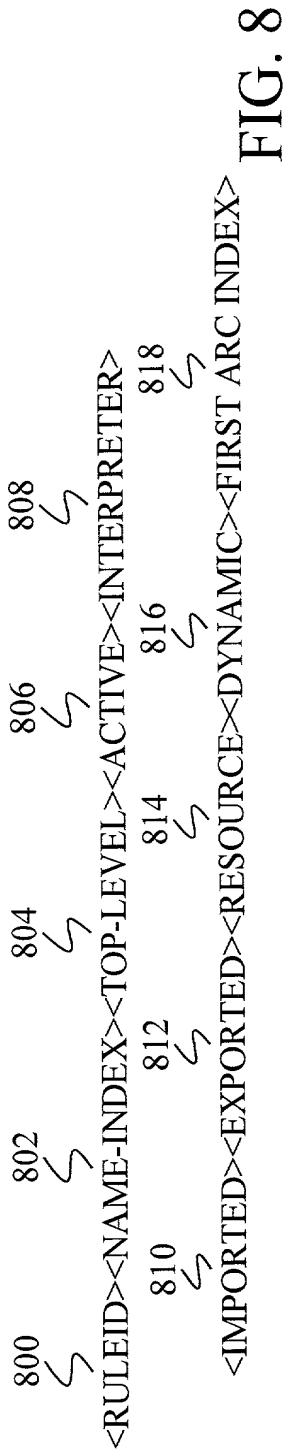


FIG. 8



FIG. 9



FIG. 10 FIG. 12

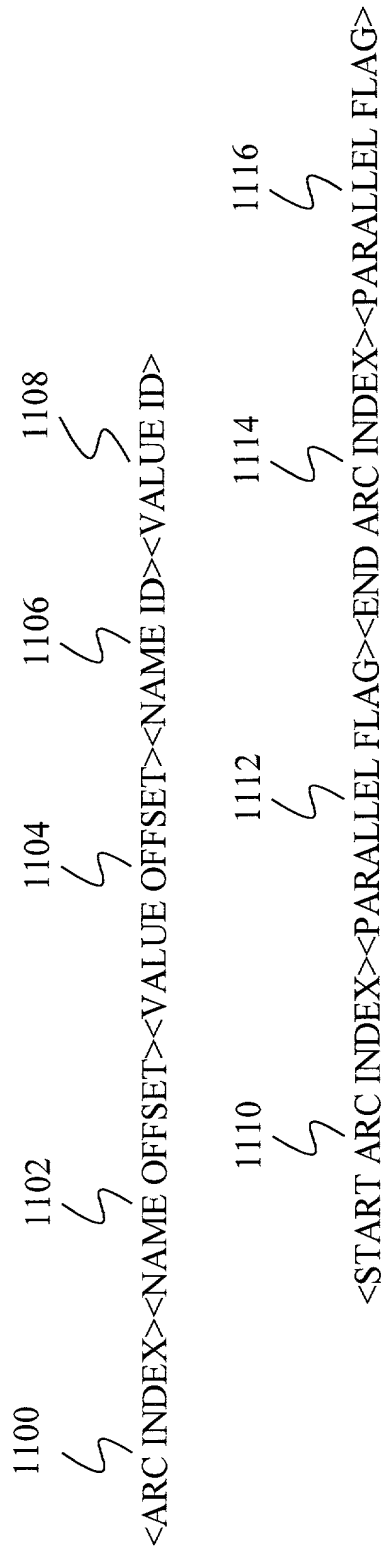


FIG. 11

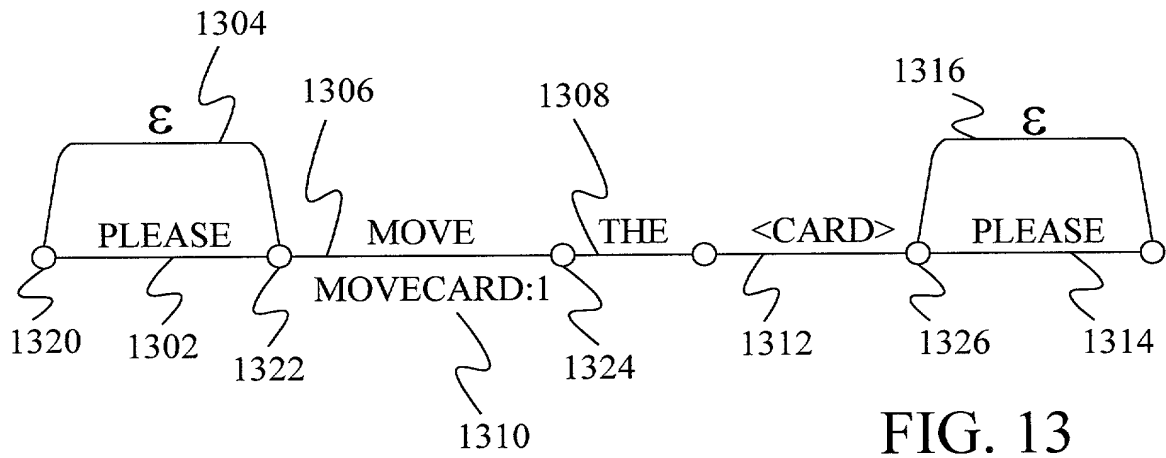


FIG. 13

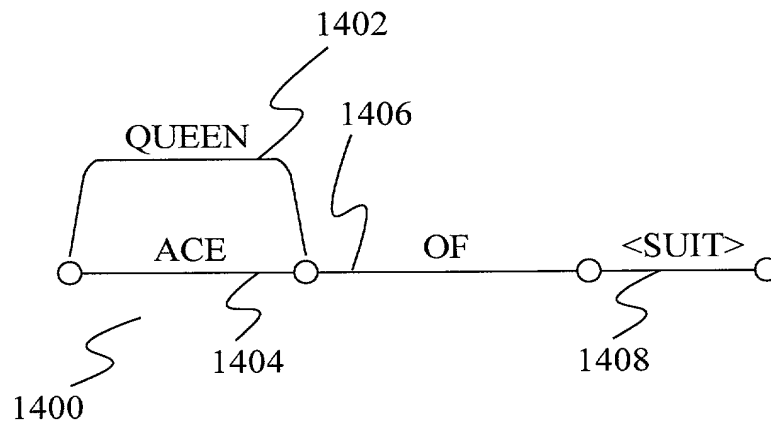


FIG. 14

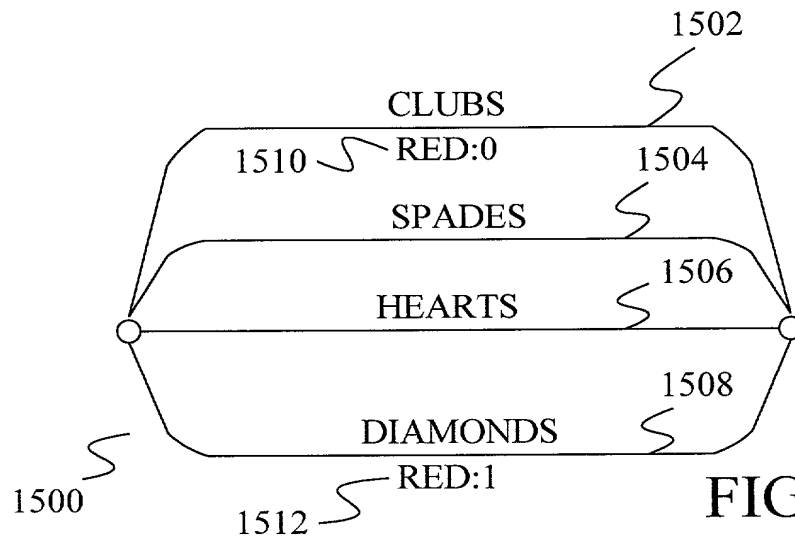


FIG. 15

1602 1604 1606 1608

ØPLEASEØMOVEØTHEØACEØQUEENØOFØCLUBSØSPADESØHEARTSØDIAMONDSØ

1600

FIG. 16

1702 1704

ØMOVEØMOVECARDØ1ØCARDØSUITØREDØ0Ø1Ø1.0Ø.8Ø.2Ø

1700

FIG. 17

1806 1808 1810 1812 1814 1816 1818 1820 1822

RULE ID	NAME INDEX	TOP	ACTIVE	INTERPT	IMPORT	EXP	RES	DYN	FIRST ARC INDEX
01	01	1	1	0	0	1	0	0	01
02	17	0	1	0	0	1	0	0	08
03	22	0	1	0	0	1	0	0	12

1800 1802 1804

FIG. 18

1900		1910	1912	1914	1916	1918
		⚡	⚡	⚡	⚡	⚡
		WORD /RULE INDEX	RULE REF	LAST	NEXT ARC	SEM TAG
1902	⚡	0	0	1	0	0
1904	⚡	0	0	0	3	0
1922	⚡	1	0	1	3	0
1924	⚡	8	0	1	4	1
1926	⚡	13	0	1	5	0
1928	⚡	2	1	1	6	0
1930	⚡	0	0	0	0	0
1932	⚡	1	0	1	0	0
1906	⚡	17	0	0	10	0
1934	⚡	21	0	1	10	0
1936	⚡	27	0	1	11	0
1938	⚡	3	1	1	0	0
1908	⚡	30	0	0	0	1
1940	⚡	36	0	0	0	0
1942	⚡	43	0	0	0	0
1944	⚡	50	0	1	0	1

FIG. 19

FIG. 20 is a schematic diagram of a system for determining a weight for an arc index. The system includes a processor 2000, a memory 2002, and a database 2004. The processor 2000 is connected to the memory 2002 and the database 2004. The processor 2000 is configured to receive an arc index from a user and to determine a weight for the arc index based on the arc index and the data stored in the database 2004. The memory 2002 is configured to store the arc index and the weight. The database 2004 is configured to store data for determining the weight.

		2004	2006
		5	5
2002	2008	ARC INDEX	WEIGHT
		1	35
		2	35
		3	35
		4	35
		5	35
		6	35
		7	35
		8	39
		9	42
		10	35
		11	35
		12	35
		13	35
		14	35
15	35		

FIG. 20

2000

2106 2108 2110 2112 2114 2116 2118 2120 2122
 4 4 4 4 4 4 4 4 4

ARC INDEX	NAME OFFSET	VALUE OFFSET	NAME ID	VALUE ID	START ARC INDEX	PARALLEL FLAG	END ARC INDEX	PARALLEL FLAG
3	6	15	100	1	2	1	7	1
12	27	31	200	0	12	0	12	0
15	27	33	200	1	15	0	15	0

2100
 2102
 2104

FIG. 21